# Lab 1: Vector Addition

## 1. Objective

The purpose of this lab is to get you familiar with using the CUDA API by implementing a simple vector addition kernel and its associated setup code.

## 2. Procedure

**Step 1:** Edit the file lab1/main.cu to implement the following where indicated:

1. Allocate device memory
2. Copy host memory to device
3. Initialize thread block and kernel grid dimensions and invoke CUDA kernel
4. Copy results from device to host
5. Free device memory

**Step 2:** Edit the file lab1/kernel.cu to implement the vector addition kernel code.

**Step 3:** Compile and test your code.

cd lab1

make

./vecadd # Uses the default vector size

./vecadd <m> # Uses vectors of size m

**Step 4:** Submit your assignment. You only need to submit the following files:

* main.cu
* kernel.cu

**3. Grading:**

Due to its simplicity, this lab will be graded on a pass/fail basis.